

- NOTES:
- 1) All resistors are 1%, 1/10W, 0603 unless otherwise specified.
 - 2) All capacitors are 0.1uF, 50V, 0603 unless otherwise specified.
 - 3) J1: Pin 7 is used as the debug connector key, it should be cut off.
 - 4) IO_VDD (1.8V) can be supplied either from J2-14 (J3 open) or VR1 (J3 shorted).
 - 5) Population:
 - a) For socketed board (U1B), C1, C2, C3 = DNP.
 - b) Secure BAT1 holder with adhesive in the rectangular soldermask area.

COMPANY: **Kionix Inc.**

TITLE: **KX23H Evaluation Board**

DRAWN: J Zappala	DATED: 09/16/2014	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED:	DATED:	DWG923	B	KAMMDM001R58	E
QUALITY CONTROL:	DATED:	SCALE: None			SHEET: 1 OF 2
RELEASED:	DATED:				

6

5

4

3

2

1

REVISION RECORD			CHECKED BY:	DATE:
DATE:	DRAWN BY:	DESCRIPTION:		
08/15/2014	J Zappala	Initial release.		
08/27/2014	J Zappala	1) Add J3. Add pull-ups to SDA_S and SCL_S. 2) Change R5 and R6 from 10K.		
09/12/2014	J Zappala	1) Rearrange diode protection connections for better layout. 2) Change 2.2K pull-ups to 2.1K; R5, R6, R7, and R8. 3) Add J4, Note 4, and U3. 3) Add Note 5.		
09/15/2014	J Zappala	1) Bring 32.768KHz clock signal out on J2-2.		
09/16/2014	J Zappala	1) Connect 32.768KHz clock signal to D1. 2) Add BAT1 holder securing to Note 5. 3) Swap J1 and J2 reference designators.		

D

D

C





C

B

B

A

A

LOGO1 Kionix Inc. LOGO — MTHOLE1  MTHOLE150 MTHOLE2  MTHOLE150 MTHOLE3  MTHOLE150 MTHOLE4  MTHOLE150	DRAWN: J Zappala	DATED: 09/16/2014	COMPANY: <p style="text-align: center; font-size: 24px;">Kionix Inc.</p> TITLE: <p style="text-align: center; font-size: 20px;">KX23H Evaluation Board</p>			
	CHECKED:	DATED:	CODE: DWG923	SIZE: B	DRAWING NO: KAMMDM001R58	REV: E
	QUALITY CONTROL:	DATED:	SCALE: None		SHEET: 2 OF 2	
	RELEASED:	DATED:				