



- NOTES:
- 1) All resistors are 1%, 1/10W, 0603 unless otherwise specified.
 - 2) All capacitors are 0.1uF, 50V, 0603 unless otherwise specified.
 - 3) J1: Pin 7 is used as the debug connector key, it should be cut off.
 - 4) IO_VDD (1.8V) can be supplied either from J2-14 (J3 open) or VR1 (J3 shorted).
 - 5) Population:
 - a) For socketed board (U1B), C1, C2, C3 = DNP.
 - b) Mount BAT1 holder after reflow. Secure BAT1 holder with adhesive in the rectangular soldermask area then solder connections.
 - c) BAT1 holder and J4 may not be required on all assemblies.

COMPANY: **Kionix Inc.**

TITLE: **KX23H Evaluation Board**

DRAWN: J Zappala	DATED: 10/08/2014	CODE:	SIZE:	DRAWING NO:	REV:
CHECKED: <Checked By>	DATED: <Checked Date>	DWG923	B	KAMMDM001R58	0
QUALITY CONTROL: <QC By>	DATED: <QC Date>	SCALE: None			SHEET: 1 OF 2
RELEASED: <Released By>	DATED: <Release Date>				

